KVR26N19D8/16
16GB 2Rx8 2G x 64-Bit PC4-2666
CL19 288-Pin DIMM

DESCRIPTION
This document describes ValueRAM's KVR26N19D8/16 is a 2G x 64-bit (16GB) DDR4-2666 CL19 SDRAM (Synchronous DRAM), 2Rx8, memory module, based on sixteen 1G x 8-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR4-2666 timing of 19-19-19 at 1.2V. Each 288-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

FEATURES
• Power Supply: VDD=1.2V Typical
• VDDQ = 1.2V Typical
• VPP = 2.5V Typical
• VDDSPD=2.2V to 3.6V
• Nominal and dynamic on-die termination (ODT) for data, strobe, and mask signals
• Low-power auto self refresh (LPASR)
• Data bus inversion (DBI) for data bus
• On-die VREFDQ generation and calibration
• Dual-rank
• On-board I2 serial presence-detect (SPD) EEPROM
• 16 internal banks; 4 groups of 4 banks each
• Fixed burst chop (BC) of 4 and burst length (BL) of 8 via the mode register set (MRS)
• Selectable BC4 or BL8 on-the-fly (OTF)
• Fly-by topology
• Terminated control command and address bus
• PCB: Height 1.23" (31.25mm)
• RoHS Compliant and Halogen-Free

SPECIFICATIONS

<table>
<thead>
<tr>
<th>Feature</th>
<th>Specification</th>
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</thead>
<tbody>
<tr>
<td>CL(IDD)</td>
<td>19 cycles</td>
</tr>
<tr>
<td>Row Cycle Time (tRCmin)</td>
<td>45.75ns(min.)</td>
</tr>
<tr>
<td>Refresh to Active/Refresh Command Time (tRFCmin)</td>
<td>350ns(min.)</td>
</tr>
<tr>
<td>Row Active Time (tRASmin)</td>
<td>32ns(min.)</td>
</tr>
<tr>
<td>Maximum Operating Power</td>
<td>TBD W*</td>
</tr>
<tr>
<td>UL Rating</td>
<td>94 V - 0</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>0° C to +85° C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-55° C to +100° C</td>
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</tbody>
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*Power will vary depending on the SDRAM used.
MODULE DIMENSIONS

All measurements are in millimeters.
(Tolerances on all dimensions are ±0.12 unless otherwise specified)

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