Memory Module Specifications

HX432C16FB3K4/128
128GB (32GB 4G x 64-Bit x 4 pcs.)
DDR4-3200 CL16 288-Pin DIMM Kit

SPECIFICATIONS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CL(IDD)</td>
<td>17 cycles</td>
</tr>
<tr>
<td>Row Cycle Time (tRCmin)</td>
<td>45.75ns(min.)</td>
</tr>
<tr>
<td>Refresh to Active/Refresh</td>
<td>350ns(min.)</td>
</tr>
<tr>
<td>Command Time (tRFCmin)</td>
<td></td>
</tr>
<tr>
<td>Row Active Time (tRASmin)</td>
<td>32ns(min.)</td>
</tr>
<tr>
<td>UL Rating</td>
<td>94 V - 0</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>0° C to +85° C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-55° C to +100° C</td>
</tr>
</tbody>
</table>

DESCRIPTION

HyperX HX432C16FB3K4/128 is a kit of four 4G x 64-bit (32GB) DDR4-3200 CL16 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 2G x 8-bit FBGA components per module. Each module kit supports Intel® Extreme Memory Profiles (Intel® XMP) 2.0. Total kit capacity is 128GB. Each module has been tested to run at DDR4-3200 at a low latency timing of 16-20-20 at 1.35V. The SPDs are programmed to JEDEC standard latency DDR4-2400 timing of 17-17-17 at 1.2V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

FACTORY TIMING PARAMETERS

- Default (JEDEC): DDR4-2400 CL17-17-17 @1.2V
- XMP Profile #1: DDR4-3200 CL16-20-20 @1.35V
- XMP Profile #2: DDR4-3000 CL16-19-19 @1.35V
- XMP Profile #3: DDR4-2666 CL19-19-19 @1.35V

FEATURES

- Power Supply: VDD = 1.2V Typical
- VDDQ = 1.2V Typical
- VPP = 2.5V Typical
- VDDSPD = 2.2V to 3.6V
- On-Die termination (ODT)
- 16 internal banks; 4 groups of 4 banks each
- Bi-Directional Differential Data Strobe
- 8 bit pre-fetch
- Burst Length (BL) switch on-the-fly BL8 or BC4(Burst Chop)
- Height 1.3425” (34.1mm), w/heatsink

Continued >>
MODULE WITH HEAT SPREADER

133.35 mm

7.2 mm

34.1 mm

MODULE DIMENSIONS

All measurements are in millimeters.
(Tolerances on all dimensions are ±0.12 unless otherwise specified)

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