Memory Module Specifications



KF552C40BBAK4-64

64GB (16GB 2G x 64-Bit x 4 pcs.)
DDR5-5200 CL40 288-Pin DIMM Kit



DEFAULT SPECIFICATIONS

CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
	32ns(min.) 94 V - 0
Row Active Time (tRASmin)	,

DESCRIPTION

Kingston FURY KF552C40BBAK4-64 is a kit of four 2G x 64-bit (16GB) DDR5-5200 CL40 SDRAM (Synchronous DRAM) 1Rx8, memory module, based on eight 2G x 8-bit FBGA components per module. Each module kit supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Total kit capacity is 64GB. Each module has been tested to run at DDR5-5200 at a low latency timing of 40-40-40 at 1.25V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

DEFAULT FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.66" (42.23mm), w/heatsink

FACTORY TIMING PARAMETERS

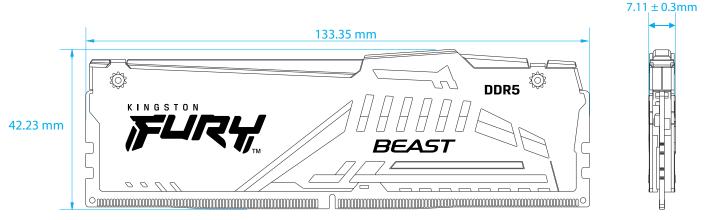
Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
 XMP Profile #1: DDR5-5200 CL40-40-40 @1.25V
 XMP Profile #2: DDR5-4800 CL38-38-38 @1.1V

Continued >>

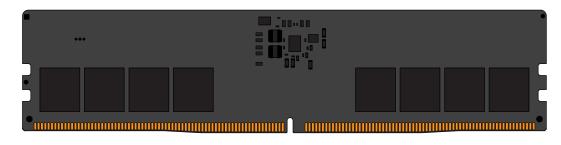
Page 1

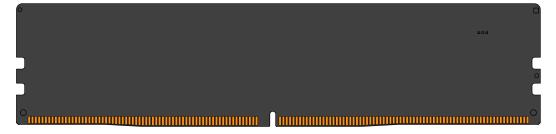


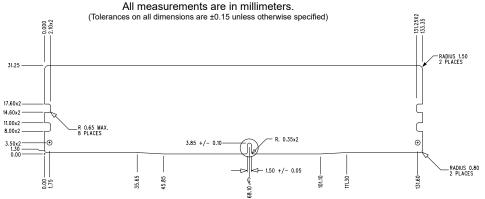
MODULE WITH HEAT SPREADER



MODULE DIMENSIONS







The product images shown are for illustration purposes only and may not be an exact representation of the product. Kingston reserves the right to change any information at anytime without notice.

FOR MORE INFORMATION, GO TO KINGSTON.COM

All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published Kingston FURY memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.