Memory Module Specifications



KF560C30BBAK2-64

64GB (32GB 4G x 64-Bit x 2 pcs.)
DDR5-6000 CL30 288-Pin DIMM Kit



SPECIFICATIONS

CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
UL Rating	94 V - 0
Operating Temperature	0° C to +85° C
Storage Temperature	-55° C to +100° C

DESCRIPTION

Kingston FURY KF560C30BBAK2-64 is a kit of two 4G x 64-bit (32GB) DDR5-6000 CL30 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 2G x 8-bit FBGA components per module. Each module kit supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Total kit capacity is 64GB. Each module has been tested to run at DDR5-6000 at a low latency timing of 30-36-36 at 1.4V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.66" (42.23mm), w/heatsink

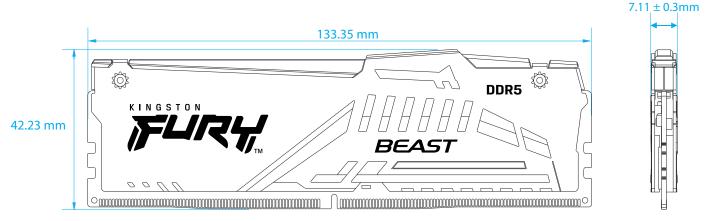
FACTORY TIMING PARAMETERS

Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
 XMP Profile #1: DDR5-6000 CL30-36-36 @1.4V
 XMP Profile #2: DDR5-5600 CL40-40-40 @1.25V
 XMP Profile #3: DDR5-4800 CL38-38-38 @1.1V

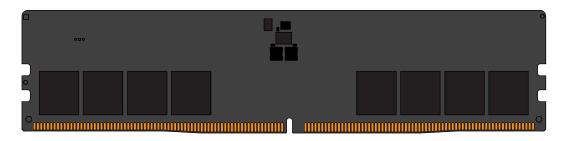
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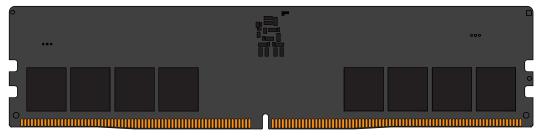


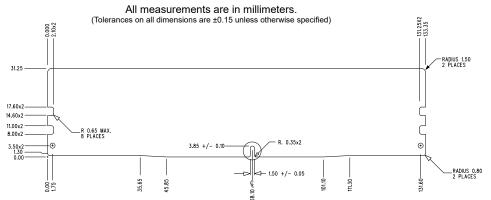
MODULE WITH HEAT SPREADER



MODULE DIMENSIONS







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All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published Kingston FURY memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.



Revision No.	History	Release Date	Remark	Editor	Approved
A	Initial Release	02/23/24		David Y.	Alex S.

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