Memory Module Specifications



KF560C40BB-32

32GB 4G x 64-Bit DDR5-6000 CL40 288-Pin DIMM



SPECIFICATIONS

OL (IDD)	10
CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
Row Active Time (tRASmin) UL Rating	32ns(min.) 94 V - 0

DESCRIPTION

Kingston FURY KF560C40BB-32 is a 4G x 64-bit (32GB) DDR5-6000 CL40 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 2G x 8-bit FBGA components per module. The module supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Each module has been tested to run at DDR5-6000 at a low latency timing of 40-40-40 at 1.35V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.37" (34.9mm), w/heatsink

FACTORY TIMING PARAMETERS

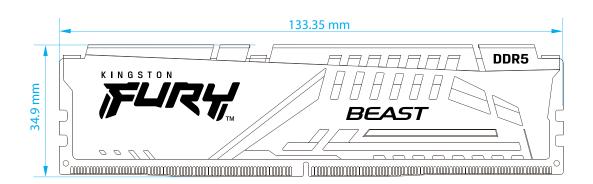
Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
 XMP Profile #1: DDR5-6000 CL40-40-40 @1.35V
 XMP Profile #2: DDR5-5600 CL40-40-40 @1.25V
 XMP Profile #3: DDR5-4800 CL38-38-38 @1.1V

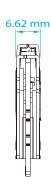
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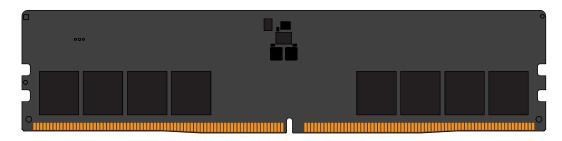


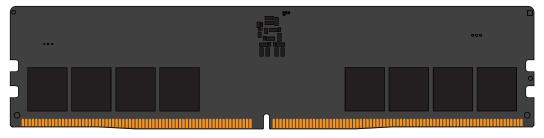
MODULE WITH HEAT SPREADER

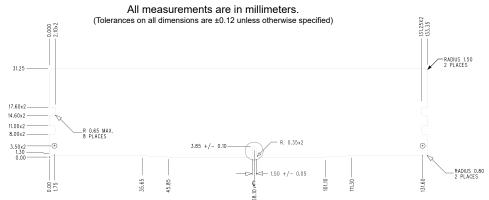




MODULE DIMENSIONS







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All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published Kingston FURY memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.