

KF564C32BBEA-32TR

32GB 4G x 64-Bit DDR5-6400 CL32 288-Pin DIMM



DEFAULT SPECIFICATIONS

CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
Row Active Time (tRASmin) UL Rating	32ns(min.) 94 V - 0
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DESCRIPTION

Kingston KF564C32BBEA-32TR is a 4G x 64-bit (32GB) DDR5-6400 CL32 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 2G x 8-bit FBGA components per module. The module supports AMD® EXPO v1.1 and Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Each module has been tested to run at DDR5-6400 at a low latency timing of 32-39-39 at 1.4V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

DEFAULT FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.66" (42.23mm), w/heatsink

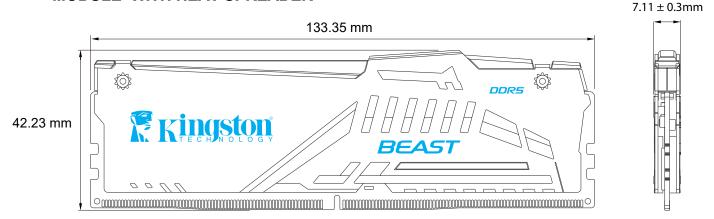
FACTORY TIMING PARAMETERS

Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
 EXPO Profile #0: DDR5-6400 CL32-39-39 @1.4V
 EXPO Profile #1: DDR5-6000 CL30-36-36 @1.4V
 XMP Profile #1: DDR5-6400 CL32-39-39 @1.4V
 XMP Profile #2: DDR5-6000 CL30-36-36 @1.4V

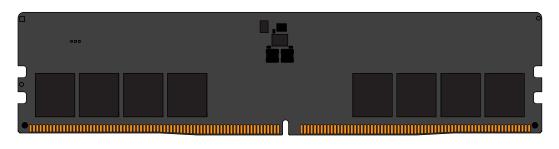
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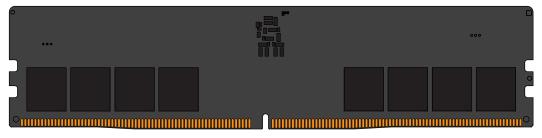
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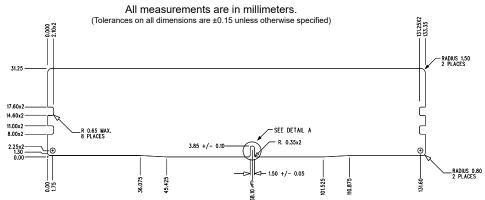
MODULE WITH HEAT SPREADER



MODULE DIMENSIONS







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