

KF564C32BBK2-64

64GB (32GB 4G x 64-Bit x 2 pcs.) DDR5-6400 CL32 288-Pin DIMM Kit



SPECIFICATIONS

CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
Row Active Time (tRASmin)	32ns(min.) 94 V - 0
	· · · · ·
UL Rating	94 V - 0

DESCRIPTION

Kingston FURY KF564C32BBK2-64 is a kit of two 4G x 64-bit (32GB) DDR5-6400 CL32 SDRAM (Synchronous DRAM) 2Rx8, memory module, based on sixteen 2G x 8-bit FBGA components per module. Each module kit supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Total kit capacity is 64GB. Each module has been tested to run at DDR5-6400 at a low latency timing of 32-39-39 at 1.4V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.37" (34.9mm), w/heatsink

FACTORY TIMING PARAMETERS

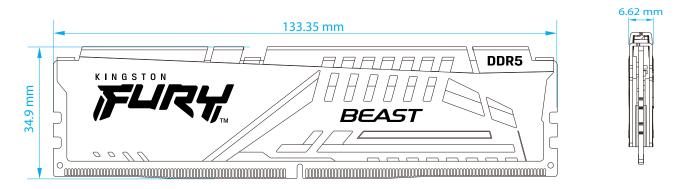
 Default (JEDEC): 	DDR5-4800 CL40-39-39 @1.1V
XMP Profile #1:	DDR5-6400 CL32-39-39 @1.4V

- XMP Profile #2: DDR5-6000 CL30-36-36 @1.4V
- XMP Profile #3: DDR5-4800 CL38-38-38 @1.1V

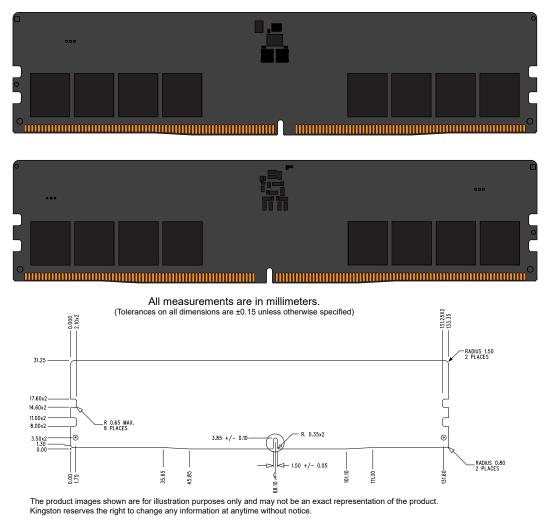
Continued >>



MODULE WITH HEAT SPREADER



MODULE DIMENSIONS



FOR MORE INFORMATION, GO TO KINGSTON.COM

All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published Kingston FURY memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.

©2024 Kingston Technology Corporation, 17600 Newhope Street, Fountain Valley, CA 92708 USA. All rights reserved. Kingston FURY and the Kingston FURY logo are trademarks of Kingston Technology Corporation. All trademarks are the property of their respective owners.

Rev. A Document No. 4811306A Page 2



Revision No.	History	Release Date	Remark	Editor	Approved	
А	Initial Release	02/15/24		David Y.	Alex S.	
*Products and specifications discussed herein are for evaluation and reference purposes only and are subject to change						
without notice. All information discussed herein is provided on an "as is" basis, without warranties of any kind.						