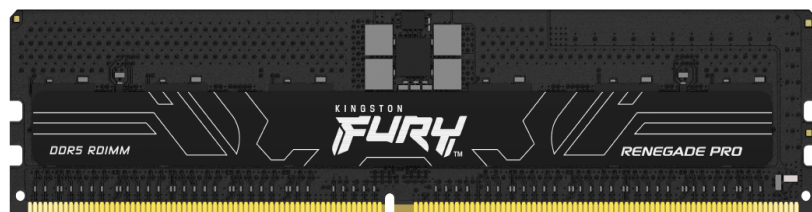


Memory Module Specifications

KF568R34RBK8-128

128GB (16GB 1Rx8 2G x 80-Bit x 8 pcs.)

PC5-6800 CL34 Registered EC8 288-Pin DIMM



DEFAULT SPECIFICATIONS

CL(IDD)	40 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	295ns(min.)
Row Active Time (tRASmin)	32ns(min.)
Row Precharge Time (tRPmin)	16ns(min.)
UL Rating	94 V - 0
Operating Temperature	0° C to +95° C
Storage Temperature	-55° C to +100° C

DESCRIPTION

Kingston FURY KF568R34RBK8-128 is a kit of eight 2G x 80-bit (16GB) DDR5-6800 CL34 SDRAM (Synchronous DRAM) 1Rx8, ECC memory module, based on ten 2G x 8-bit FBGA components per module. The module supports Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Total kit capacity is 128GB. Each module has been tested to run at DDR5-6800 at a low latency timing of 34-44-44 at 1.4V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

DEFAULT FEATURES

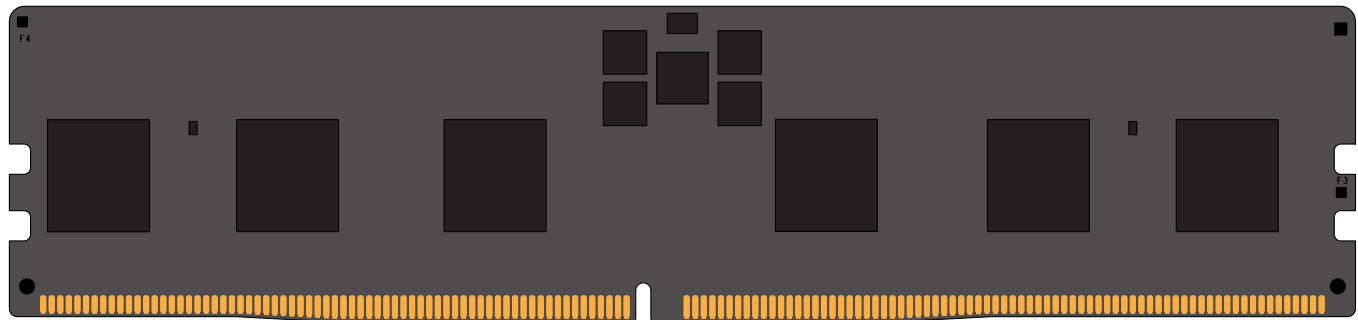
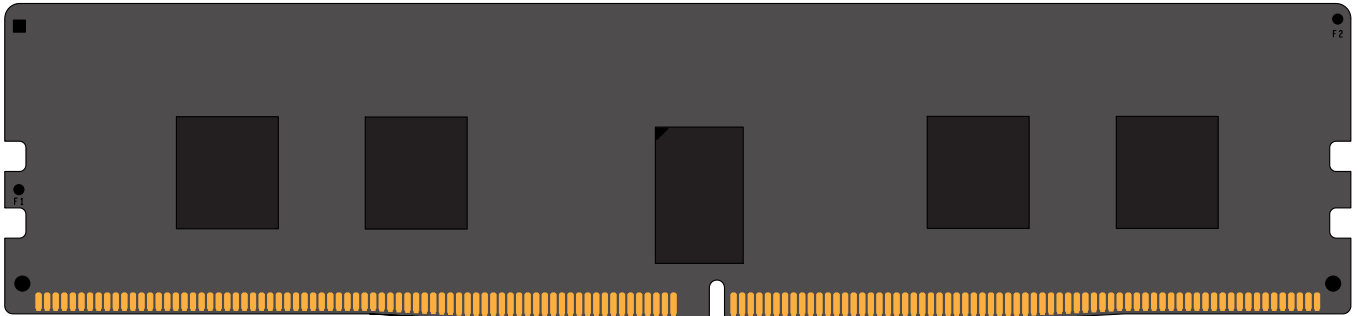
- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- x80 ECC (x40, 2 independent I/O sub channels)
- 32 internal banks
- Hard/Soft Post Package Repair
- Sideband access with I3C/I2C
- PCB: Height 1.23" (31.25mm)
- RoHS Compliant and Halogen-Free

FACTORY TIMING PARAMETERS

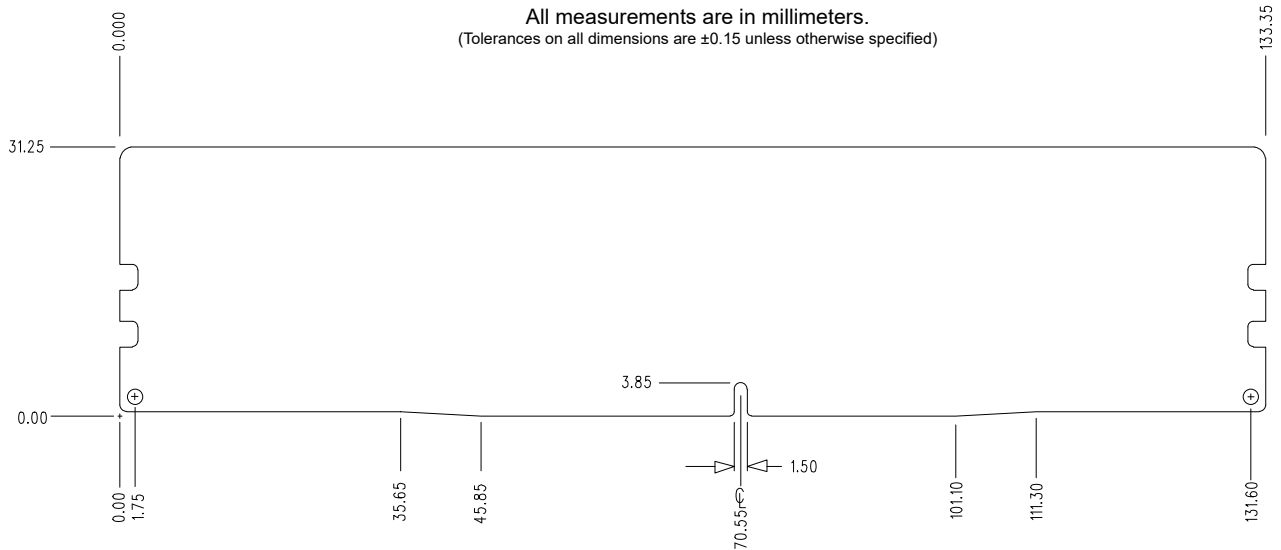
- Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
- XMP Profile #1: DDR5-6800 CL34-44-44 @1.4V
- XMP Profile #2: DDR5-6400 CL32-39-39 @1.4V
- XMP Profile #3: DDR5-4800 CL36-38-38 @1.1V

Continued >>

MODULE DIMENSIONS



All measurements are in millimeters.
(Tolerances on all dimensions are ± 0.15 unless otherwise specified)



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