

Memory Module Specifications

KSM64R52BD4-96MC

96GB 2Rx4 12G x 80-Bit

PC5-6400 CL52 Registered EC8 288-Pin DIMM

DESCRIPTION

Kingston's KSM64R52BD4-96MC is a 12G x 80-bit (96GB) DDR5-6400 CL52 SDRAM (Synchronous DRAM), 2Rx4, ECC, memory module, based on forty 6G x 4-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR5-6400 timing of 52-52-52 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

FEATURES

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- x80 ECC (x40, 2 independent I/O sub channels)
- 32 internal banks
- Hard/Soft Post Package Repair
- Sideband access with I3C/I2C
- PCB: Height 1.23" (31.25mm)
- RoHS Compliant and Halogen-Free

SPECIFICATIONS

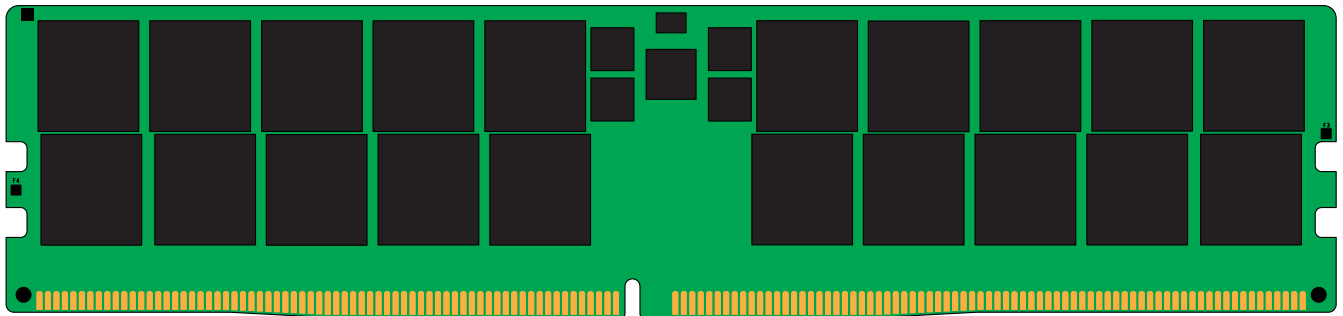
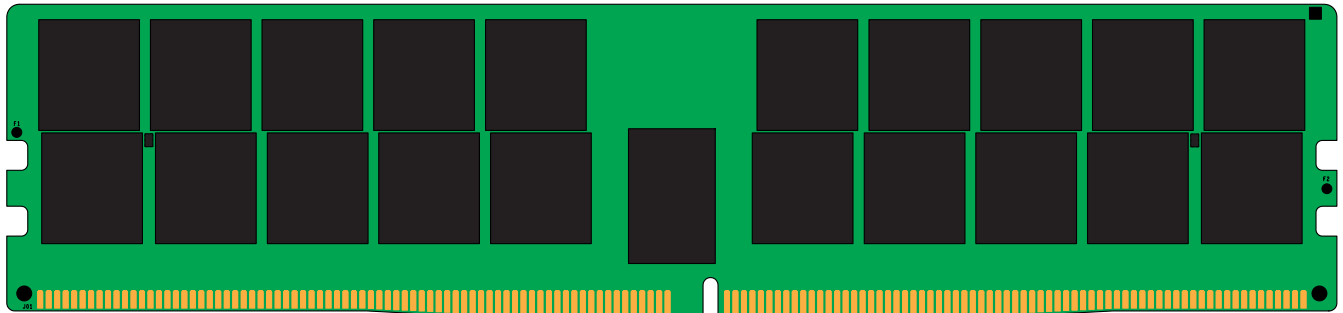
CL	52 cycles
Row Cycle Time (tRCmin)	48ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	410ns(min.)
Row Active Time (tRASmin)	32ns(min.)
Row Precharge Time (tRPmin)	16ns(min.)
UL Rating	94 V - 0
Operating Temperature	0° C to +95° C
Storage Temperature	-55° C to +100° C

Module Assembly

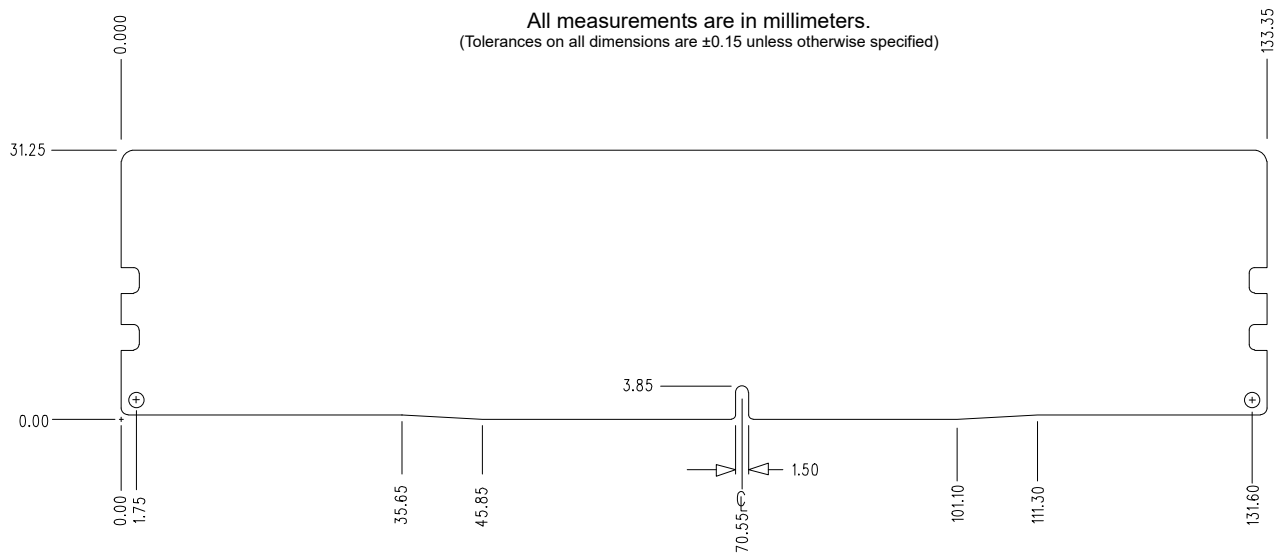
DRAM: Micron (C-DIE)

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MODULE DIMENSIONS



All measurements are in millimeters.
(Tolerances on all dimensions are ± 0.15 unless otherwise specified)



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